

FIG. 1 is a block diagram of a data processing system 100. The system 100 includes a plurality of processing units 102, each of which includes a data input/output (DIO) block 104, an encoder 106, a decoder 108, a data input/output (DIO) block 110, and a feedback block 112. The DIO blocks 104 and 110 are connected to a data bus 122. The encoder 106 and decoder 108 are connected to the DIO blocks 104 and 110, respectively. The feedback block 112 is connected to the DIO block 110 and the DIO block 104. The system 100 also includes a clock signal Φ_1 and a clock signal Φ_2 .

100

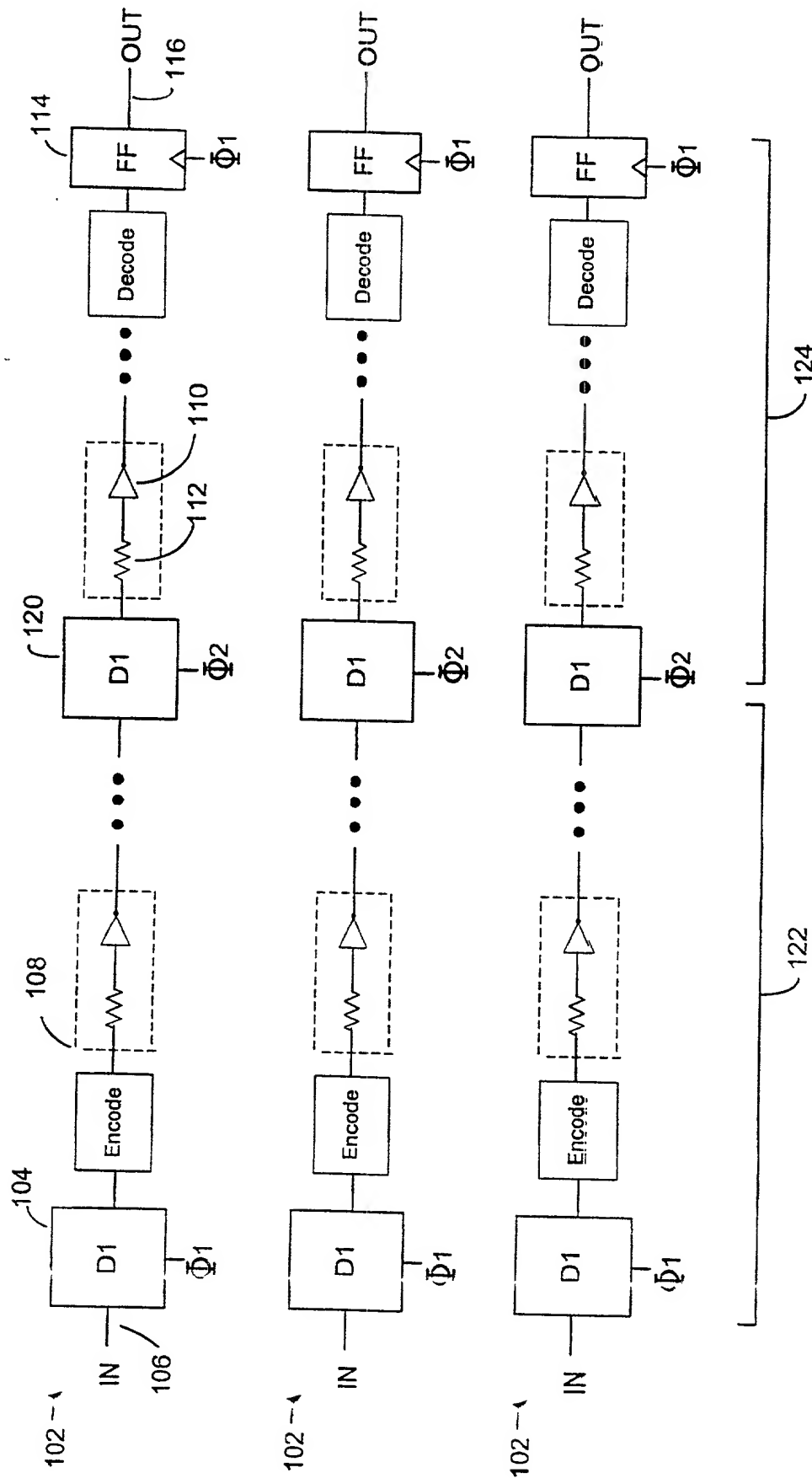


FIG. 1

200 ↗

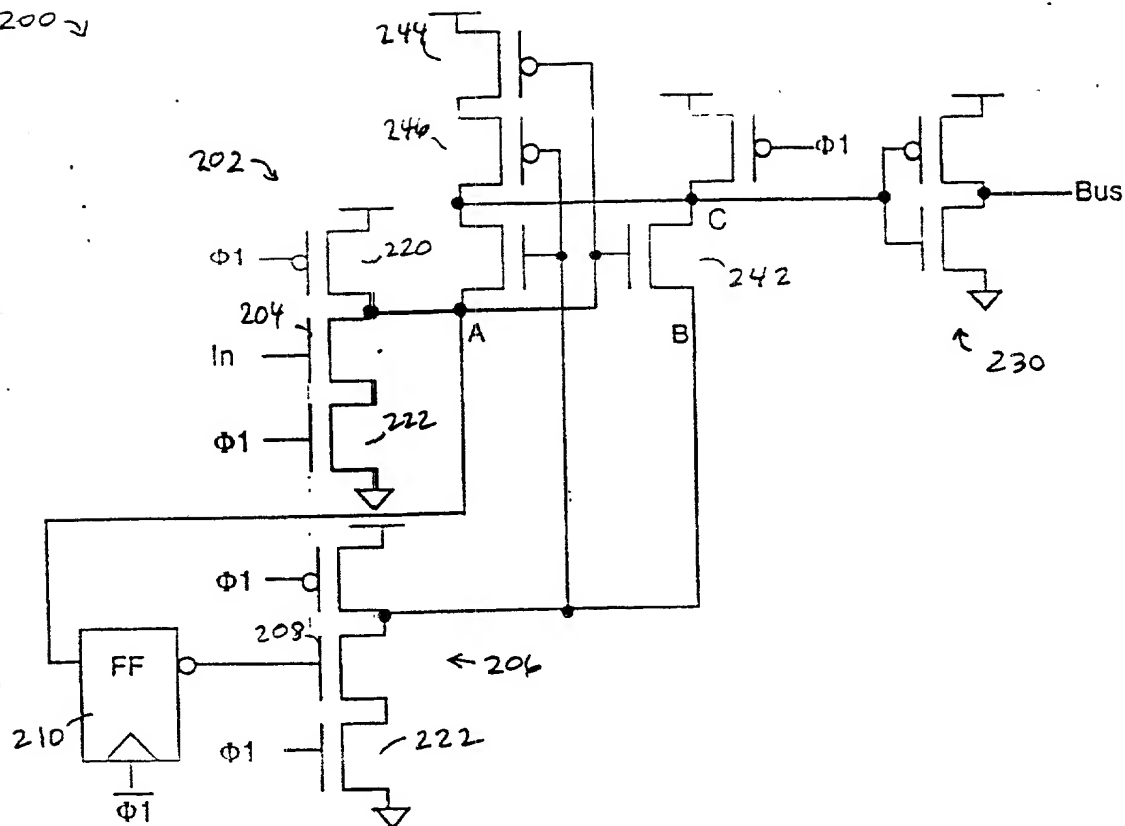


FIG. 2

300 ↗

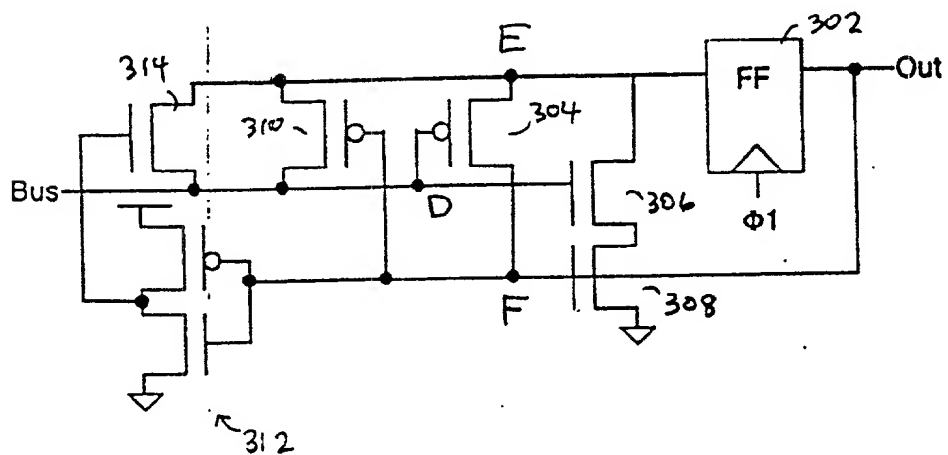


FIG. 3